



**33rd EDS/CAS Activities in Western New York Conference
Monday Nov 2nd, 2009**

Agenda

- 10:30AM** Conference Registration
- 11:00** Opening Remarks
- 11:05** *(Invited)* “A Study of NBTI and PBTI (Charge Trapping) in High k Stacks with Metal Gates”, S. Zafar, IBM T.J. Watson Research Center, Yorktown Heights, NY
- Noon** **Lunch**
- 1:05PM** *(Invited)* “OMEN: a quantum transport simulator for nano-electronic devices”, M. Luisier, Network for Computational Nanotechnology, Purdue University
- 1:50** “3D TCAD design and Simulation of SOI multi-gate MOSFET”, P. Feng, Y. Li and P. Ghosh, Department of Electrical Engineering and Computer Science, Syracuse University, NY
- 2:15** “Hybrid Polymer/Ultrathin Porous Nanocrystalline Silicon Membrane for Chemical Vapor and Gas Sensing”, M. Kavalenka¹, D.Z. Fang¹, C.C. Striemer^{1,3}, J.L. McGrath² & P.M. Fauchet¹ ¹ Department of Electrical and Computer Engineering, ²Department of Biomedical Engineering, University of Rochester, Rochester, NY; ³Simpore, Inc., West Henrietta NY
- 2:40** **Coffee Break / Poster Setup**
- 3:10** *(Invited)* “The Future of Semiconductor Lithography beyond 32nm”, B.W. Smith, Microsystems Engineering, Rochester Institute of Technology, Rochester, NY
- 3:35** “Fabrication of Chiral Metallic Nanostructures with Two Photon Lithography for Application in Metamaterials”, S. Shukla and P.N. Prasad, Department of Electrical Engineering, University at Buffalo, State University of New York
- 4:00** “CMOS Design and evaluation of a MEMS thermal actuator for viscosity measurements”, I. Puchades and L.F. Fuller, Department of Electrical and Microelectronic Engineering, RIT
- 4:25** Poster “Flash Nano” Session
- 4:50 – 6:15PM** **Poster Session / Reception**