

RITCHIE

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OBJECTIVE

Graduate electrical engineering student interested in Analog/ Mixed Signal Design. Seeking a summer 2020 intern or co-op.

EDUCATION

Master of Science in Electrical Engineering | GPA: 3.11/4 **Expected May 2021**

Rochester Institute of Technology, Rochester, NY

Coursework: Analog Electronics Design, Advanced carrier Injection Devices, Microelectronic Fabrication; (In process) Engineering Analysis, Mixed Signal IC design, Advanced Field Effect Devices

Bachelor in Technology in Electrical Engineering | CGPA: 7.88/10 **May, 2018**

Vallabhbhai National Institute of Technology, Surat, India

SKILLS

Technical Skill: Design of CMOS operational Amplifier, Brokaw Band gap Reference, Comparator Design, Flash, ADC Design, SAR ADC, Switch Capacitor, Phase Lock Loop (PLL)

Computer Skill: Cadence Virtuoso, MATLAB, Python, C

INTERNSHIP

Siemens Corp | Bengaluru, India (**May, 2018 - June, 2018**)

- Assisted with the operation and maintenance of various electrical equipment such as induction motors, circuit breakers and transformers throughout the customer plant. Documented results in daily operational report.

PROJECTS

Design of two stage CMOS operational Amplifier

Sept. 2019 – Nov. 2019

- Design of two stage CMOS operational amplifier using 1 V power supply and 45 nm technology.
- Power consumption passed all DRC and PVS steps.
- Circuit passed all process corners and Monte Carlo corners.

Design of Band Gap Reference Circuit Using Brokaw Cell

Jan. 2019 – April 2019

- Design of constant voltage reference circuit using Brokaw cell as reference circuit. Combination of PTAT (Proportional to absolute temperature) and CTAT (complementary to absolute temperature) is used.
- Start-up circuit designed to give stability to the circuit during transient stages.

Design of PMOS Transistor

Aug. 2018 – Dec. 2018

- PMOS transistor of parameter W/L (10/60) is designed and fabricated.
- The project was completed in SMFL. This fabrication consists of different steps process like diffusion, Ion implantation, lithography, step etch processes, stain and groove experiment, metal implantation and different measurement steps.

EXTRA CURRICULAR ACTIVITIES

- Member of RIT IEEE Student Chapter
- Volunteer at RIT Brick City Homecoming 2019
- Chess and Cricket